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G Radin - Proceedings of the first international symposium on ..., 1982 - portal.acm.org  
 ... **degen-erate case** (or can **move the entire ... to be an appropriate language for writing**  
**systems programs ... common sub-expression elimination, • moving code out of ...**  
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A Comparkon of Full and Partial Predicated Execution Support for ILP Processors -  
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SA Mahlke, RE Hank, JE McCormick, DI August, WW ... - portal.acm.org  
 ... **more conditional moves** for example ... to allow the instruction to execute and to dis-  
 allow any change of processor state in the **write-back stage ... predicated code. ...**  
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Method for inserting new machine instructions into preexisting machine code to  
monitor preexisting ... - group of 3 »

R Hastings - US Patent 5,335,344, 1994 - Google Patents  
 ... **I6> v. HEADER INSTRUCTIONS AND DATA SYMBOLS ... 100 r10 210 CORRECT SYMBOLS AND**  
**RELOCATION**  
**STRUCTURES f m WRITE NEW OBJECT CODE FILE L \_ SELECT FIRST ITEM ...**  
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... correct processing of pipelined instructions including branch instructions and self-  
modifying code ... - group of 3 »

ET Grochowski, DB Alpert - US Patent 5,692,167, 1997 - Google Patents  
 ... **Maintain Coherency Move Write Instruction of Code Cache 24 Into EX Stage 18 504**  
**502 CompareAddresses ofWrite Instructions With Addresses of Instructions in ...**  
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VCODE: a retargetable, extensible, very fast dynamic code generation system - group  
 of 4 »

DR Engler - Proceedings of the ACM SIGPLAN 1996 conference on ..., 1996 - portal.acm.org  
 ... **of the core** (eg, conditional **move**, floating-point ... Currently VCODE creates **code one**  
**function at a ... A sample VCODE specification to dynamically create a function ...**  
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A semantic model of types and machine instructions for proof-carrying code - group  
 of 11 »

AW Appel, AP Felty - Proceedings of the 27th ACM SIGPLAN-SIGACT symposium on ..., 2000 -  
 portal.acm.org  
 ... **We can now write the step relation** (l; m) H (#, m') (Figure 3 ... it possible to model  
**function-pointer types and moves complexity from the code consumer to ...**  
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[PS] Analyzing Mobile Code Languages - group of 7 »

G Cugola, C Ghezzi, GP Picco, G Vigna - Mobile Object Systems: Towards the Programmable Internet,  
 1997 - elet.polimi.it  
 ... **a mobile agent are dynamically replicated by move to the destination CE together**  
**with the agent code and execution ... for the data space of the moving EU, Ty ...**  
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Code compression - group of 6 »

J Ernst, W Evans, CW Fraser, TA Proebsting, S ... - Proceedings of the ACM SIGPLAN 1997

conference on ..., 1997 - portal.acm.org  
... for common operations such as **moving** and initializing ... the base integer register **move**  
instruction as ... Opcode combination captures common **code** generation idioms. ...  
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**MOVE: a framework for high-performance processor design**

H Corporaal, HJM Mulder - Proceedings of the 1991 ACM/IEEE conference on ..., 1991 - portal.acm.org  
... not be big, because of the large sequential **code** compo- nent ... of departure, the de-  
velopment of the **MOVE** concept can ... Reducing bypass read-and-write connections ...  
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**Global code motion/global value numbering - group of 3 »**

C Click - Proceedings of the ACM SIGPLAN 1995 conference on ..., 1995 - portal.acm.org  
... GCM will **move code** from main paths into more control dependent paths. ... The **code** on  
the left is the original pro- ... 21 ], Chris Vick has an excellent **write-up** of ...  
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**PowerPC Architecture - Memory Management Unit (MMU)**

The **Memory Management Unit** (MMU) provides **Address** Translation, Protection Functions, and Storage Attribute **Control** for this **address** space. ...  
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Title: **Memory management unit** for managing **address** operations corresponding to domains using environmental **control**. Document Type and Number: ...  
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A n-channel **memory management** circuit operates as an interface **unit** between ... The expanded **memory address** is used to generate or **control** instruction which ...  
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**Using the Microprocessor MMU for Software Protection in Real-Time ...**

First, the prices of high end microprocessors with **Memory Management Units** ... **control**

circuitry and pass the **address** directly onto the system **memory** bus. ...  
www.linuxworks.com/products/whitepapers/mmu.php3 - Similar pages

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RB Tremaine, PA Franaszek, JT Robinson, CO Schulz, ... - IBM Journal of Research and Development, 2001 - researchweb.watson.ibm.com  
 ... soon as a minimum subcache-line unit of data ... in operating system (BIOS) or system  
 management program software. ... to toggle the physical main-memory address space ...

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C Cheng, SL Porter, WC Richardson, PJ Roy - US Patent 5,155,844, 1992 - Google Patents  
 ... f NV MEMORY ... complete 46, control returns to step 42 to test additional ... system; and  
 a second data structure connected to the memory management unit and the ...

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CC Nei, DR Hafeman, W Fazakerly - US Patent 4,787,061, 1988 - Google Patents  
 ... The present invention supports a time queue management algorithm which is ... pointer  
 array in the Event Queue memory 60 ... Note that in both timing and unit delay mode ...

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JM Lasker, JM McGillis - US Patent 5,586,291, 1996 - Google Patents  
 ... and more particularly to cache memory management in disk ... The controller micropro-  
 20 cesser generally coordinates and ... of central processor units (CPUs) and ...

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K Satou - US Patent 5,886,793, 1999 - Google Patents  
 ... memory 18, and the above-mentioned memory management information is ... transfer report  
 to the printing unit 8 to be ... facsimile machine 25 also has a buffer memory. ...

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R Nagaraj, MS Shipman - US Patent 6,125,236, 2000 - Google Patents  
 ... unit that includes a system management mode (SMM ... memory unit and includes a prestored  
 interrupt processing ... media parameter control functions that provide a user ...

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R Bannatyne - Vehicular Technology Conference, 1999 IEEE 49th, 1999 - ieeeexplore.ieee.org  
 ... will in turn require even more electronic controls for power management. ... and hydraulic  
 subsystems interfaced together with a control unit. ... Flash EEPROM Memory ...

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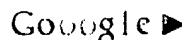
A Chip, HDRW LAN, PCCWLAN Adapters, USBWLAN ... - ngia.rootforge.org  
 ... Output, 2mA Low (or only) Byte Memory Write Enable ... Not Used By Firmware) T/R\_SW -  
 Transmit/Receive Control ... PARAMETER SYMBOL TEST CONDITIONS MIN TYP MAX UNITS ...

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